

# DECLARATION

Dr./Mr./Ms. \_\_\_\_\_ is an employee / research scholar/student of our institution and is permitted to attend the Faculty Development Program on VLSI & MEMS Design conducted by Department of Electronics and Communication Engineering, IIT Senapati, Manipur from February 10th – 14th, 2020.

**Place:**

**Date:**

**Signature**

**Head of the Institution/Department/Section  
with office seal**